



PNP Silicon Transistor

SOT-23

# **Description**

### • General small signal amplifier

## **PIN Connection**

### **Features**

- Low collector saturation voltage: V<sub>CE(sat)</sub>=-0.3V(Max.)
- Low output capacitance : Cob=4pF(Typ.)
- Complementary pair with KC5343S



Type NO.	Marking	Package Code
KA1980S	CA 🗆 🗀 👵	SOT-23
	1 2 3	301 23

①Device Code ②HFE Grade ③Year& Week Code • Dalian

Ta=25 C

# **Absolute Maximum Ratings**

Characteristic	Symbol	Ratings	Unit
Collector-base voltage	Vсво	-50	V
Collector-emitter voltage	Vceo	-50	V
Emitter-base voltage	<b>V</b> EBO	-5	V
Collector current	<b>I</b> c	-150	mA
Collector power dissipation	Pc*	350	mW
Junction temperature	Tj	150	°C
Storage temperature range	Tstg	-55~150	°C

<sup>\*</sup> Package mounted on 99.5% alumina 10×8×0.6mm

### **Electrical Characteristics**

Ta=25 C

Characteristic	Symbol	Test Condition	Min.	Typ.	Max.	Unit
Collector-emitter breakdown voltage	BVceo	Ic=-1mA, I <sub>B</sub> =0	-50	-	-	٧
Collector cut-off current	Ісво	Vcb=-50V, IE=0	-	-	-0.1	μΑ
Emitter cut-off current	<b>I</b> EBO	V <sub>EB</sub> =-5V, I <sub>C</sub> =0	-	-	-0.1	μΑ
DC current gain	h <sub>FE</sub> *	Vce=-6V, Ic=-2mA	70	-	700	ı
Collector-emitter saturation voltage	VCE(sat)	Ic=-100mA, Iв=-10mA	-	-	-0.3	>
Transition frequency	fτ	Vce=-10V, Ic=-1mA	80	-	-	MHz
Collector output capacitance	Сор	V <sub>CB</sub> =-10V, I <sub>E</sub> =0, f=1MHz	-	4	-	pF
Noise figure	NF	$V_{CE}$ =-6V, $I_{C}$ =-0.1Ma $f$ =1KHz, $Rg$ =10K $\Omega$	-	10	-	dB

<sup>\*:</sup> hfe rank / O : 70~140, Y : 120~240, G : 200~400, L : 300~700.

#### **Electrical Characteristic Curves**

Fig. 1 Pc - Ta

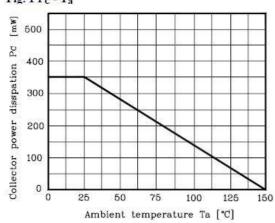
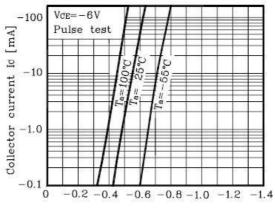


Fig. 2 I<sub>C</sub> - V<sub>BE</sub>



Base-emitter voltage VBE [V]

Fig. 3  $I_C - V_{CE}$ 

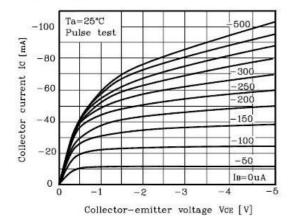


Fig. 4 h<sub>FE</sub> - I<sub>C</sub>

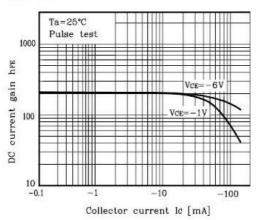
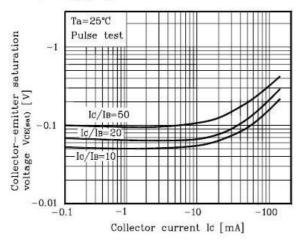
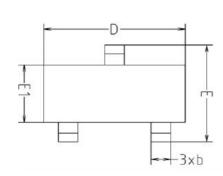
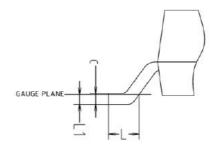


Fig. 5 V<sub>CE(sat)</sub> - I<sub>C</sub>

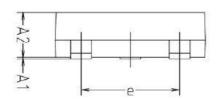


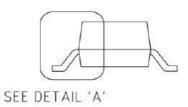
# **Outline Dimension**





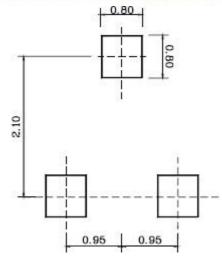
DETAIL 'A'





SYMBOL	MILLIMETERS			NOTE
	MINIMUM	NOMINAL	MAXIMUM	NOTE
Α1	0.00	2	0.10	
Α2	0.82	- 4	1.02	
Ь	0.39	0.42	0.45	
C	0.09	0.12	0.15	
D	2.80	2.90	3.00	
E	2.20	2.40	2.60	
E1	1.20	1.30	1.40	-
е	5	1.90BS0	T T	
L	0.20	-	-	
L1		0.12BS0		

## \*Recommend PCB solder land [Unit: mm]



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