

KTB1188
PNP Silicon Transistor

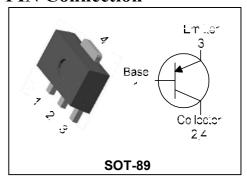
Description

• Medium power amplifier

Features

- P_C (Collector power dissipation)=1W (Ceramic substrate of 250 mm × 0.8t used)
- Low collector saturation voltage : V_{CE(sat)}=-0.5V (Typ.)
- Complementary pair with KTD1766

PIN Connection



Ordering Information

Type NO.	Marking		Package Code
KTB1188	B1 □YWW•	_	SOT-89
B1: Device code. THEF Grade YWW (Y: Ye	ear code. WW: Week code	Dalian)	

Absolute maximum ratings

(Ta=25 C)

Characteristic	Symbol	Ratings	Unit	
Collector-Base voltage	V_{CBO}	-40	V	
Collector-Emitter voltage	V_{CEO}	-32	V	
Emitter-Base voltage	V_{EBO}	-5	V	
Collector current	${ m I}_{ m C}$	-2	А	
Collector newer dissipation	P _C	0.5	W	
Collector power dissipation	P _C *	1	VV	
Junction temperature	Tı	150	°C	
Storage temperature	T _{stg}	-55~150	°C	
Operating temperature range	T _{opr}	-40~125	°C	

^{*:} When mounted on ceramic substrate (250 m²×0.8t)

KTB1188

Electrical Characteristics

(Ta=25 C)

Characteristic	Symbol	Test Condition	Min.	Typ.	Max.	Unit
Collector-Base breakdown voltage	BV _{CBO}	$I_C=-50~\mu\text{A},~I_E=0$	-40	-	-	V
Collector-Emitter breakdown voltage	BV _{CEO}	$I_{\text{C}}{=}{-}1$ mA, $I_{\text{B}}{=}0$	-32	-	-	V
Emitter-Base breakdown voltage	BV _{EBO}	$I_E=-50 \ \mu A, \ I_C=0$	-5	ı	-	\
Collector cut-off current	I_{CBO}	V _{CB} =-20V, I _E =0	ı	ı	-1	μA
Emitter cut-off current	I _{EBO}	V _{EB} =-4V, I _C =0	ı	1	-1	μA
DC current gain	h _{FE} *	V _{CE} =-3V, I _C =-0.1A	100	-	320	-
Collector-Emitter saturation voltage	$V_{CE(sat)}$	$I_{\text{C}}\text{=-2A, }I_{\text{B}}\text{=-200 mA}$	-	-0.5	-0.8	V
Transition frequency	f⊤	V_{CB} =-5 V , I_{C} =-500 mA, f =30 MHz	-	150	-	MHz
Collector output capacitance	C _{ob}	V _{CB} =-10V, I _E =0, f=1 MHz	-	50	-	рF

^{*:} h_{FE} rank / O: 100~200, Y: 160~320

Electrical Characteristic Curves

Fig. 1 P_C - T_a

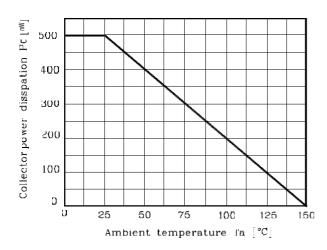


Fig. 2 I_C - V_{BE}

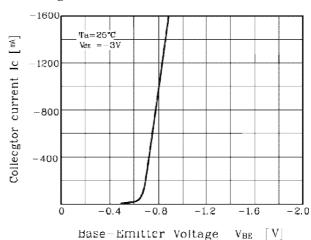


Fig. 3 I_C - V_{CE}

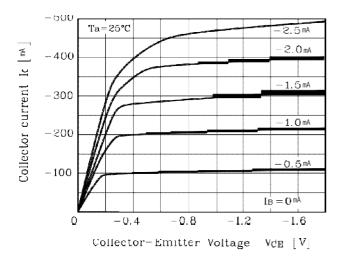


Fig. 4 $V_{CE(sat)}$ - I_C

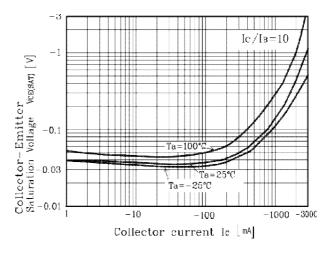


Fig. 5 h_{FE} - I_{C}

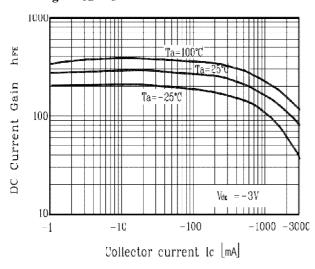
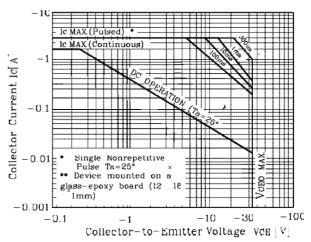
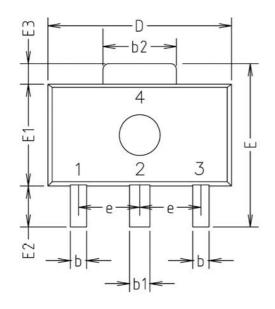
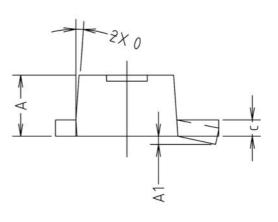


Fig. 6 Safe Operating Area



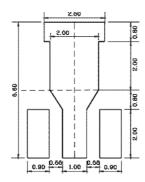
Outline Dimension (Unit: mm)





		NOTE			
SYMBOL	MINIMUM	NOMINAL	MAXIMUM	NOTE	
Α	1.40	1.50	1.60		
A1	0.00	_	0.10		
b	0.38	0.42	0.48		
b1	0.48	0.52	0.58		
b2	1.79	1.82	1.87		
С	0.40	0.42	0.46		
D	4.40	4.50	4.70		
Ε	3.70	4.00	4.30		
E1	2.40	2.50	2.70		
E2	0.80	1.00	1.20		
E3	0.40	0.50	0.60		
е		1.50 TYP.	(
0		4° TYP.			

%Recommend PCB solder land (Unit: mm)



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