

SWITCHING REGULATOR APPLICATIONS

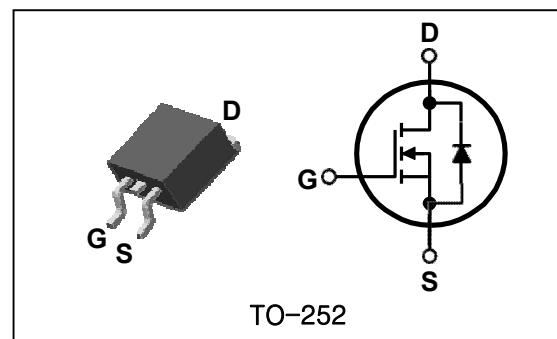
Features

- High Voltage : $BV_{DSS}=500V$ (Min.)
- Low C_{rss} : $C_{rss}=33pF$ (Typ.)
- Low gate charge : $Q_g=16nC$ (Typ.)
- Low $R_{DS(on)}$: $R_{DS(on)}=1.5\Omega$ (Max.)

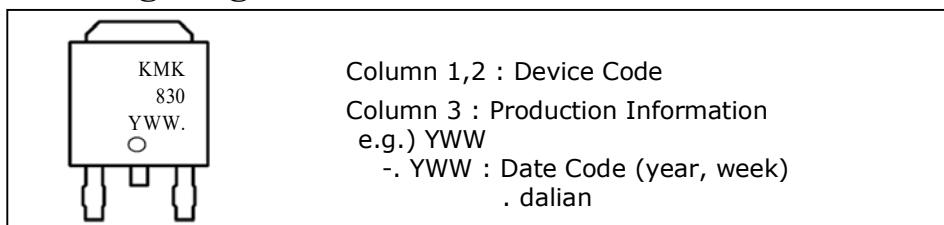
Ordering Information

Type No.	Marking	Package Code
KMK830D	KMK830.	TO-252
	. Dalian	

PIN Connection



Marking Diagram



Absolute maximum ratings ($T_c=25^\circ C$ unless otherwise noted)

Characteristic	Symbol	Rating	Unit
Drain-source voltage	V_{DSS}	500	V
Gate-source voltage	V_{GSS}	± 30	V
Drain current (DC) *	I_D	($T_c=25^\circ C$)	A
		($T_c=100^\circ C$)	A
Drain current (Pulsed) *	I_{DM}	18	A
Power dissipation	P_D	48	W
Avalanche current (Single) ②	I_{AS}	4.5	A
Single pulsed avalanche energy ②	E_{AS}	250	mJ
Avalanche current (Repetitive) ①	I_{AR}	4.5	A
Repetitive avalanche energy ①	E_{AR}	5.0	mJ
Junction temperature	T_J	150	$^\circ C$
Storage temperature range	T_{stg}	-55~150	

* Limited by maximum junction temperature

Characteristic	Symbol	Typ.	Max.	Unit
Thermal resistance	$R_{th(J-C)}$	-	2.6	$^\circ C/W$
	$R_{th(J-A)}$	-	50	

** When mounted on the minimum pad size recommended (PCB Mount)

Electrical Characteristics ($T_C=25^\circ\text{C}$ unless otherwise noted)

Characteristic	Symbol	Test Condition	Min.	Typ.	Max.	Unit
Drain-source breakdown voltage	BV_{DSS}	$I_D=250\mu\text{A}, V_{GS}=0$	500	-	-	V
Gate threshold voltage	$V_{GS(\text{th})}$	$I_D=250\mu\text{A}, V_{DS}=V_{GS}$	2.0	-	4.0	V
Drain-source cut-off current	I_{DSS}	$V_{DS}=500\text{V}, V_{GS}=0\text{V}$	-	-	1	μA
Gate leakage current	I_{GSS}	$V_{DS}=0\text{V}, V_{GS}=\pm 30\text{V}$	-	-	± 100	nA
Drain-source on-resistance ⁽⁴⁾	$R_{DS(\text{ON})}$	$V_{GS}=10\text{V}, I_D=2.25\text{A}$	-	1.2	1.5	$\text{m}\Omega$
Forward transfer conductance ⁽⁴⁾	g_{fs}	$V_{DS}=10\text{V}, I_D=2.25\text{A}$	-	5.2	-	S
Input capacitance	C_{iss}	$V_{GS}=0\text{V}, V_{DS}=25\text{V}, f=1\text{MHz}$	-	745	930	pF
Output capacitance	C_{oss}		-	82	102	
Reverse transfer capacitance	C_{rss}		-	33	42	
Turn-on delay time	$t_{d(\text{on})}$	$V_{DD}=250\text{V}, I_D=4.5\text{A}$ $R_G=25\Omega$	-	12	-	ns
Rise time	t_r		-	46	-	
Turn-off delay time	$t_{d(\text{off})}$		-	50	-	
Fall time	t_f		-	48	-	
Total gate charge	Q_g	$V_{DS}=400\text{V}, V_{GS}=10\text{V}$ $I_D=4.5\text{A}$	-	16	20	nC
Gate-source charge	Q_{gs}		-	5.5	-	
Gate-drain charge	Q_{gd}		(3)(4)	-	4.0	

Source-Drain Diode Ratings and Characteristics ($T_C=25^\circ\text{C}$ unless otherwise noted)

Characteristic	Symbol	Test Condition	Min.	Typ.	Max.	Unit
Source current (DC)	I_s	Integral reverse diode in the MOSFET	-	-	4.5	A
Source current (Pulsed) ⁽¹⁾	I_{sM}		-	-	18	
Forward voltage ⁽⁴⁾	V_{SD}	$V_{GS}=0\text{V}, I_s=4.5\text{A}$	-	-	1.4	V
Reverse recovery time	t_{rr}	$I_s=4.5\text{A}, V_{GS}=0\text{V}$ $dI_F/dt=100\text{A}/\mu\text{s}$	-	263	-	ns
Reverse recovery charge	Q_{rr}		-	1.9	-	uC

Note :

⁽¹⁾ Repetitive rating : Pulse width limited by maximum junction temperature

⁽²⁾ $L=22.2\text{mH}, I_{AS}=4.5\text{A}, V_{DD}=50\text{V}, R_G=25\Omega$, Starting $T_J=25^\circ\text{C}$

⁽³⁾ Pulse Test : Pulse width $\leq 300\text{us}$, Duty cycle $\leq 2\%$

⁽⁴⁾ Essentially independent of operating temperature

Electrical Characteristic Curves

Fig. 1 I_D - V_{DS}

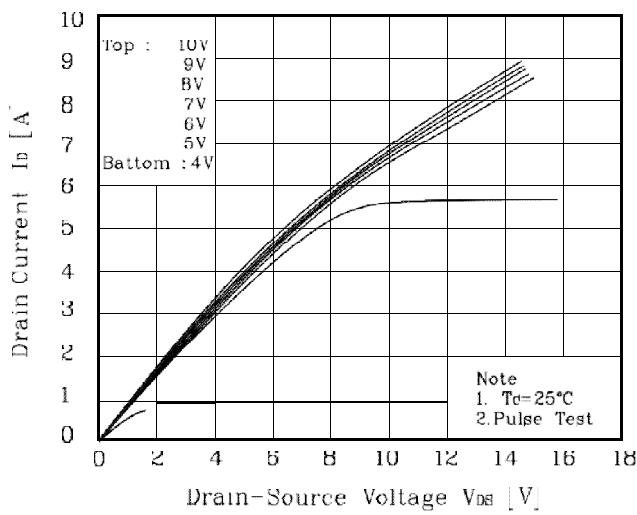


Fig. 2 I_D - V_{GS}

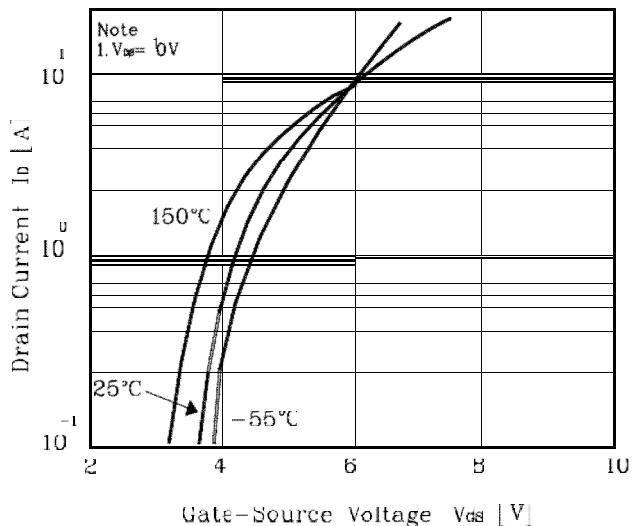


Fig. 3 $R_{DS(on)}$ - I_D

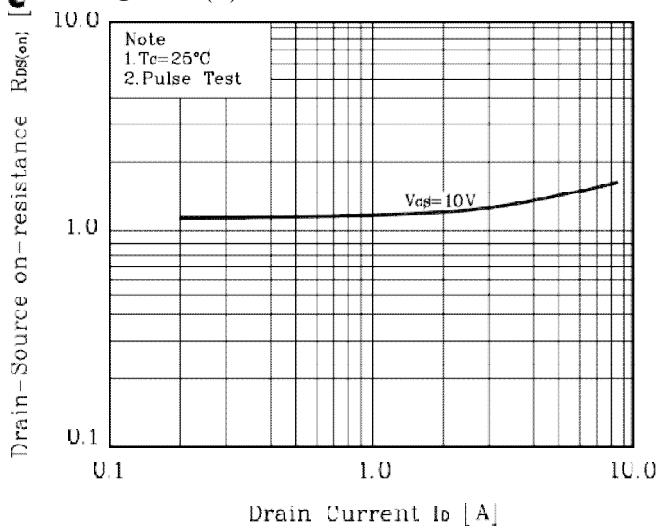


Fig. 4 I_S - V_{SD}

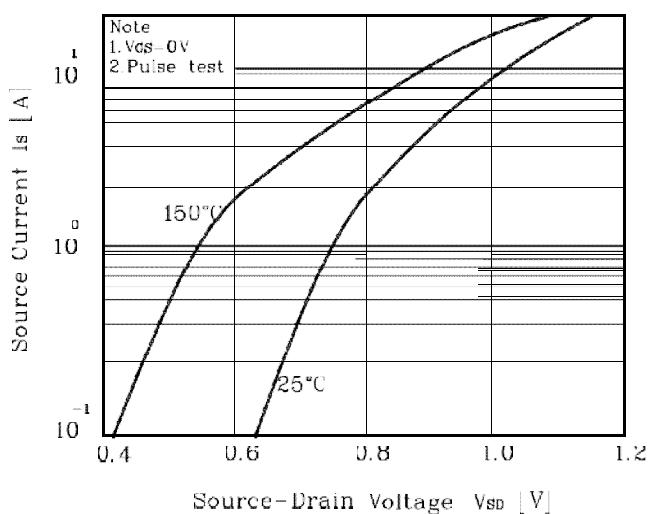


Fig. 5 Capacitance - V_{DS}

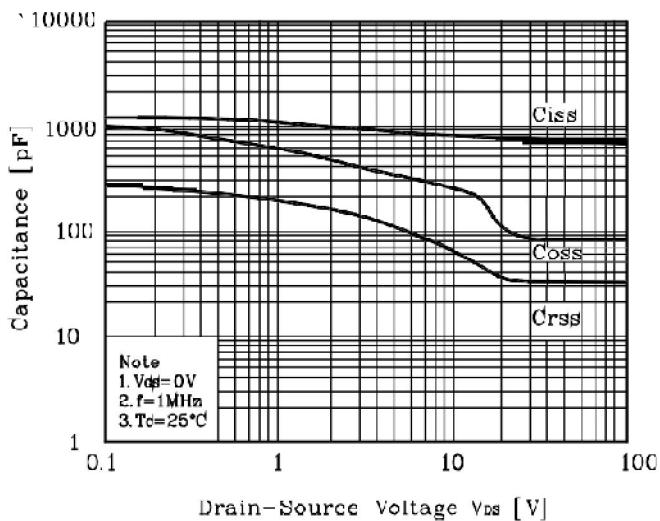


Fig. 6 V_{GS} - Q_G

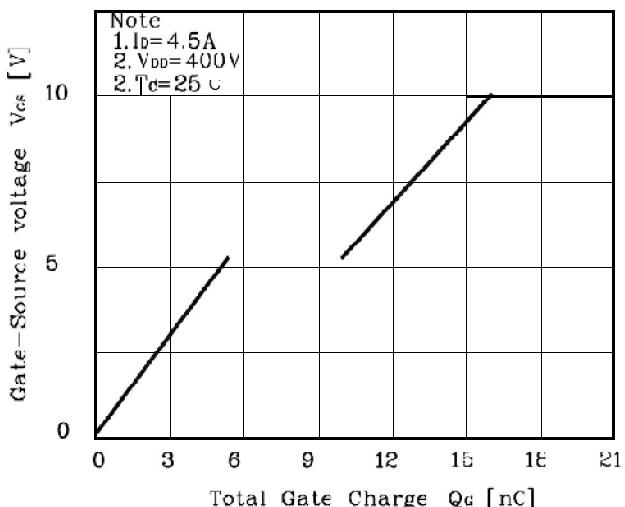


Fig. 7 $V_{(BR)DSS}$ - T_J

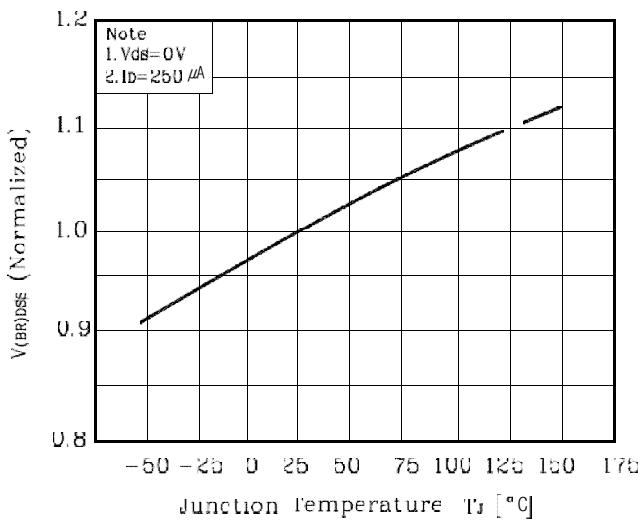


Fig. 8 $R_{DS(on)}$ - T_J

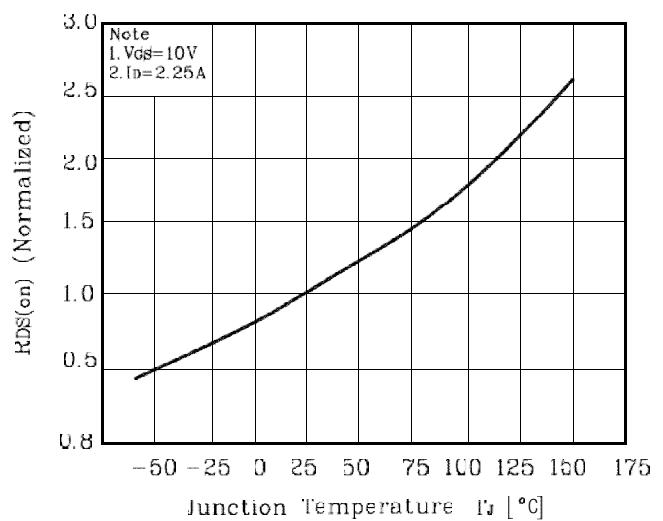


Fig. 9 I_D - T_C

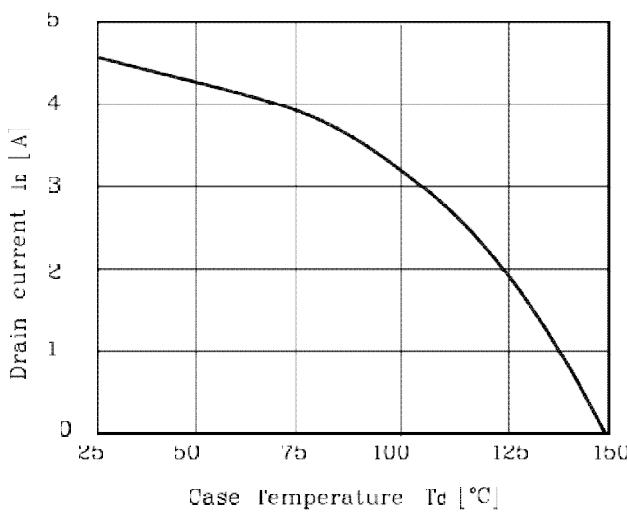


Fig. 10 Safe Operating Area

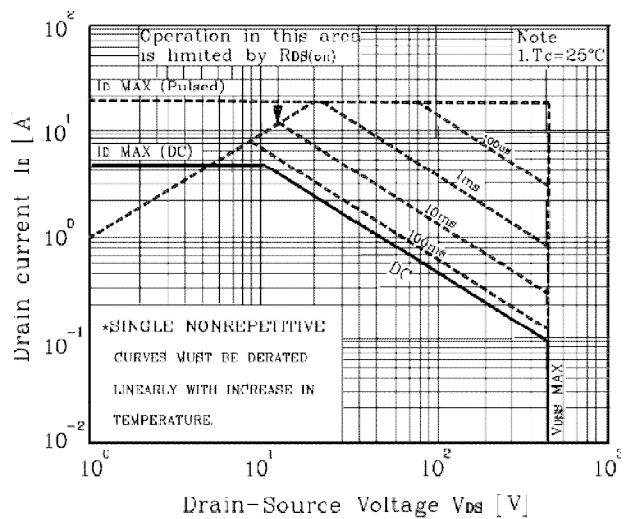


Fig. 11 Gate Charge Test Circuit & Waveform

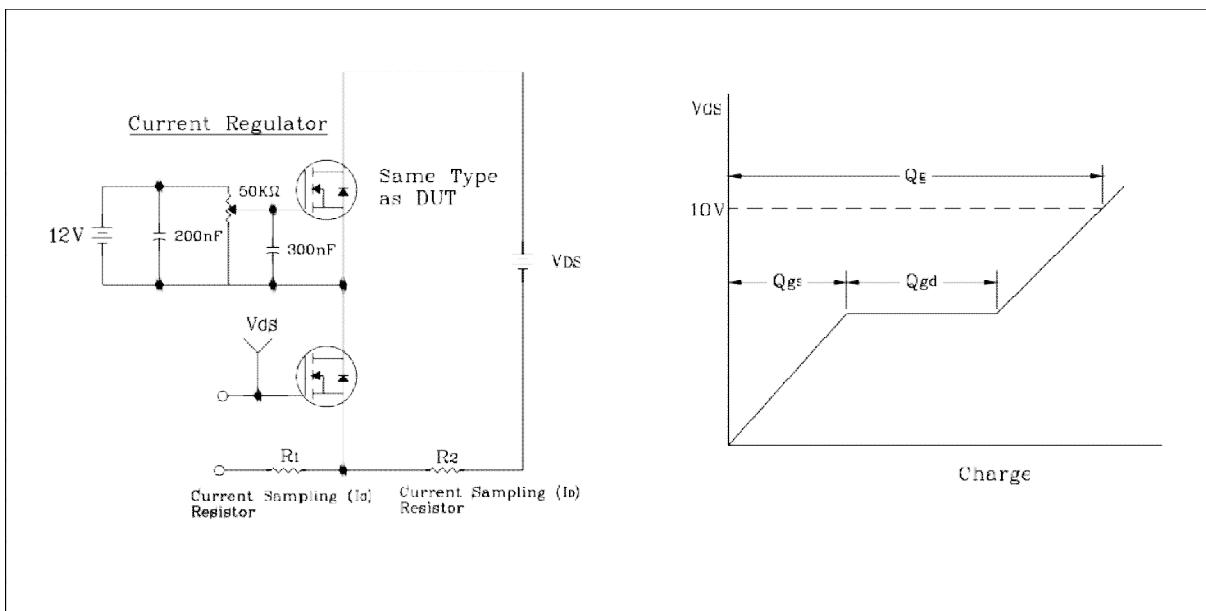


Fig. 12 Resistive Switching Test Circuit & Waveform

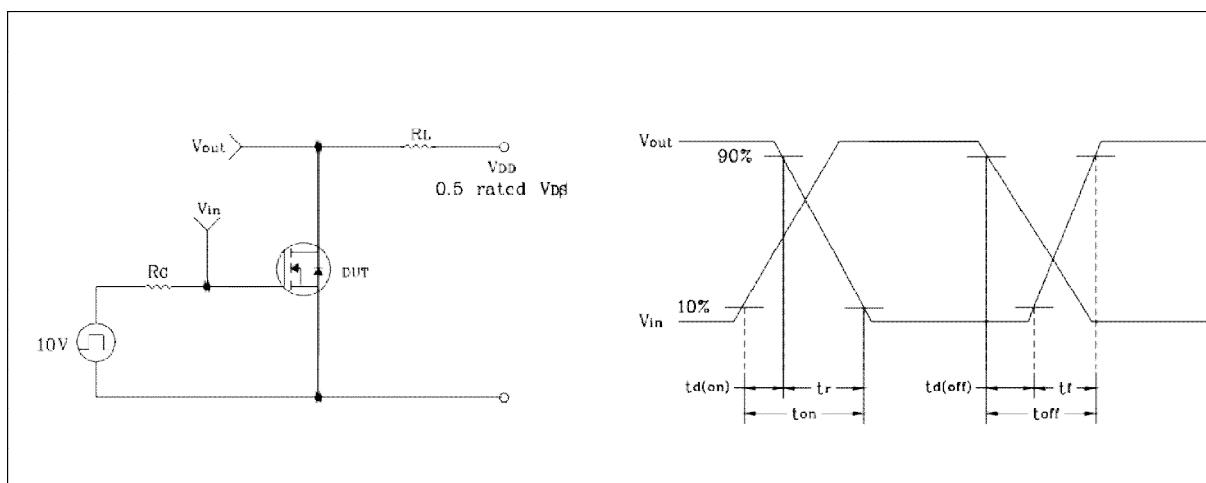


Fig. 13 E_{AS} Test Circuit & Waveform

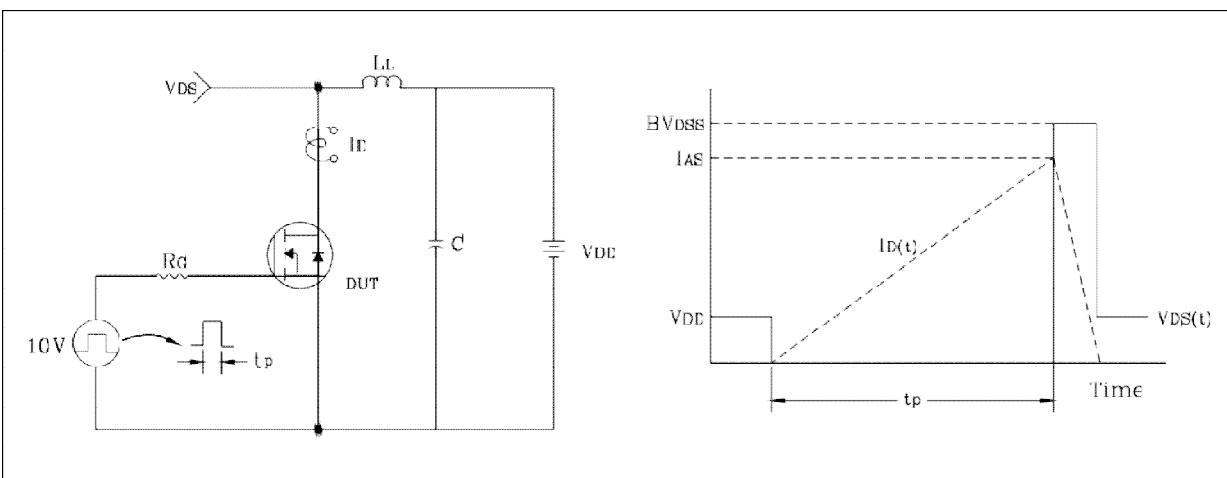
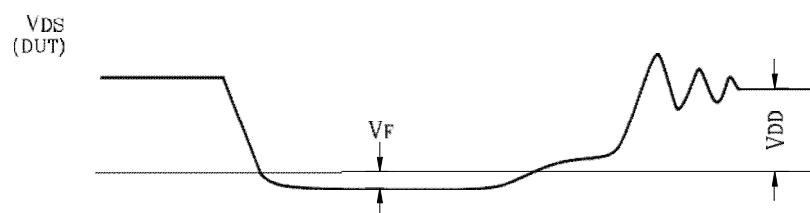
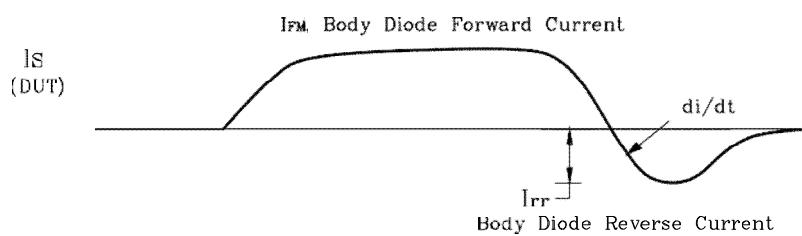
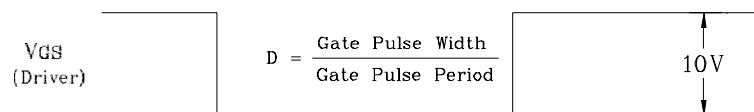
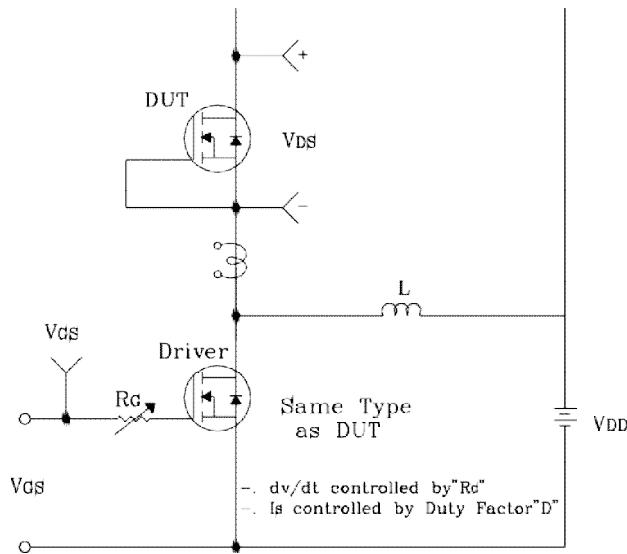
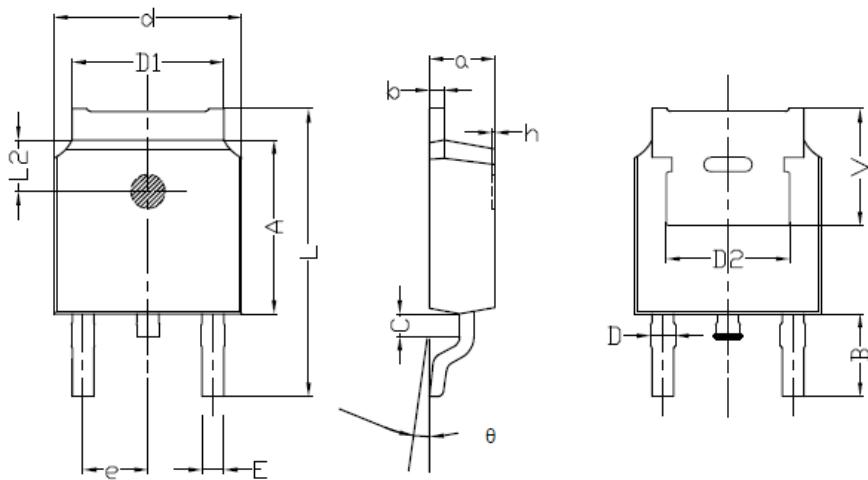


Fig. 14 Diode Reverse Recovery Time Test Circuit & Waveform



Outline Dimension

unit: mm



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	min.	max.	min.	max.
a	2.20	2.40	0.087	0.095
b	0.46	0.58	0.018	0.023
c	0.70	0.90	0.028	0.035
D	0.80	1.00	0.032	0.039
d	6.30	6.70	0.248	0.264
D1	5.00	5.50	0.197	0.217
D2	TYP 4.83		TYP 0.190	
A	5.80	6.20	0.228	0.244
e	2.19	2.39	0.086	0.094
L	9.40	10.40	0.370	0.409
B	2.6	3.2	0.102	0.126
L2	1.5	1.8	0.059	0.071
θ	0	8	0	8
h	0	0.3	0	0.012
V	5.25	5.85	0.207	0.230
E	0.6	0.8	0.024	0.032

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